

# GOVERNMENT COLLEGE OF ENGINEERING, JALGAON (An Autonomous Institute of Government of Maharashtra)

Department of Electronics and Telecommunication Engineering

## Minutes of the Meeting of Board of Studies (BoS) in E&Tc Engineering

The ~~meeting~~ <sup>(13)</sup> meeting of all the members of Board of Studies in Electronics and Telecommunication Engineering of this institute was held on 13/10/2022 at 03.00 pm in the online mode using Google-meet platform. The following faculty members were present for the meeting.

Sr. No.	Names of the Faculty Members
1.	Prof. Dr. D. S. Chaudhari - Chairperson
2.	Prof. Dr. S. V. Bonde - Member
3.	Prof. Dr. Upena Dalal - Member
4.	Prof. Dr. S. S. Narkhede - Member
5.	Dr. S. P. Mohani - Member
6.	Shri M. S. Sadavarte - Member (Invited)
7.	Smt. S. C. Jadhav - Member (Invited)
8.	Smt. M. R. Dhotre - Member (Invited)
9.	Shri D. M. Gaikwad - Member (Invited)
10.	Dr. S. C. Kulkarni - Member Secretary

At the outset, Chairperson-BoS welcomed all faculty members for the meeting. Member-Secretary-BoS informed all the members that Dr. R. A. Patil has expressed his inability to attend the meeting due to some unavoidable circumstances, and therefore leave of absence was granted to him.

*With the permission of the chairperson, the proceedings of BoS started.*

### Item No 1

To discuss and approve the proposed curriculum structure of B. Tech. program in Electronics and Telecommunication Engineering 2022 curriculum, to be implemented from academic year 2022-23) prepared by the Departmental Faculty Board (DFB).

BoS noted and discussed the revised curriculum structure of the B. Tech. program (Electronics and Telecommunication Engineering prepared by the DFB. After a through discussion on the proposed curriculum structure, BoS approved the same with following modifications:

- It was noted that courses namely Embedded Systems and VLSI Design are offered in the seventh and the eighth (VIII) semester respectively in the proposed curriculum structure.
- BoS members suggested that it will be more appropriate to swap these courses and to offer VLSI Design in the seventh (VII) semester and Embedded Systems in the eighth (VIII) semester. After detailed discussion, BoS approved this modification.
- BoS members also suggested including a professional course on "Fundamentals of Cloud Computing" in the eighth (VIII) semester, as professional elective-V. BoS agreed and resolved to include this course in the eighth (VIII) semester.

The proposed curriculum structure of B. Tech. program in Electronics and Telecommunication Engineering (2022 curriculum, to be implemented from academic year 2022-23), was approved by the BoS with the above modifications and is enclosed as Annexure-I.

**Item No 2**

To discuss and approve detailed syllabi of the following courses.

1. ET151R Basic Electronics and Measurement Techniques
2. ET152R Basic Electronics and Measurement Techniques Lab

Detailed syllabi of courses ET151R Basic Electronics and Measurement Techniques and ET152R Basic Electronics and Measurement Techniques Lab for F. Y. B. Tech. were approved after thorough discussion with minor modifications. Approved syllabi of these courses are enclosed as **Annexure –II**.

With the consent of Chairperson, meeting concluded with a vote of thanks by Member Secretary.

*Sekubson*  
18.10.2022

Member Secretary - BoS

*[Signature]*  
Chairperson - BoS E&TC

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- Copy To:**
1. All members of BoS - E&Tc Engineering, GCoE, Jalgaon.
  2. Hon. Principal and Chairman – Academic Council, GCoE, Jalgaon .
  3. Hon. Chairman, Academic Programme Evaluation Committee, GCoE, Jalgaon