Suhas S. Gajre Ph.D. (IIT Delhi) / सुहास गाजरे

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Education:

10 th : 1984	12 th : 1986	BE: 1990	ME: 1995	PhD: 2007
82.71% - 12 th in Div. Board	92.67% - 2 nd in Div. Board	77.53% - 2 nd in Univ	78.69% - 2 nd in Univ	IIT Delhi #

Studies on Electroimpedance and Electromyographic Signals to Assess Early Diagnosis of Knee Osteoarthritis Noninvasively (Prof. Sneh Anand, CBME, IITD)

Career milestones:

Oct-1990	Oct-2000	Oct-2005	Nov-2005	Nov-2007	Jun-2015	Nov-2023
Lecturer	Lecturer (Sr. Scale)	Lecturer (Sel. Grade)	Assistant Prof.	Associate Prof.	Professor	Principal

Membership of Professional Bodies: Fellow of IETE, Fellow of IE(I) and life member of ISTE

Administrative Experience:

Principal, Government College of Engineering, Jalgaon (9-Nov-2023 till date)

Head, Dept. of Electronics and	Dean (Industry Liaison) and	Dean (Incubation) and	Dean (Innovation, Incubation
Telecomm. Engineering	In-charge TPO	Dean (Procurement)	and Linkages)
9-Jul-2014 to 8-Jul-2016	11-Jun-2018 to 2-Aug-2020	1-Oct-2020 to 31-Jul-2023	1-Aug-2023 to 7-Nov-2023
Overall in-charge of dept., supervision of dept. activities, Chairman BoS of the dept., interface between dept. stakeholders and top management, etc.	Institute-Industry Liaison; Institute- Alumni Liaison; In-charge of Training and Placement, Technology Innovation and Entrepreneurship Center, Incubation, Engineering Exploration Lab.	Overall in-charge of innovation, incubation and entrepreneurship activities, In-charge of TIEC, Incharge of Innovation & EE Lab, Incharge of procurement activities in the Institute	Overall in-charge of innovation, incubation and entrepreneurship activities, Technology Innovation and Entrepreneurship Center (TIEC), Engineering Exploration Lab
Achievements:	Achievements:	Achievements:	Achievements:
New M.Tech. (ES&VLSI) course (intake of 18) started in 2015-16 Increased intake of existing M.Tech. (Electronics) from 18 to 30 in 2015-16 Digitization of notices using Google Classroom (well before Covid-19) Visit to UTP, Malaysia for joint research projects	 4th Rank in ARIIA 2020 in Govt. and Govtaided category Incubations: SolutionsTouch ('21), Cosmers ('20), Aqualnformatics ('18) MOU and Networking: AIC-RMP, CMIA-MAGIC, GIZ-MASSIA, NIMA, DISQ, Chipspirit, etc. SIH and other events: Many SIH winners (recent in 2022), SAE-BAJA, DIPEX, Robocon, etc. Facilitated 1 yr e-Vehicle project designed, and developed inhouse with 30-member team 	Sanction of AICTE IDEA Lab with funding of Rs. 110 Lakh (Rs. 55 Lakh from AICTE) worked as mentor for winning team of SIH 2022 Hardware at Reva Univ., Bengaluru Working as member of MAGIC's Seed Fund Investment Recommendation Committee Working with 'GMG Innovation Academy', an initiative of Garje Marathi Global Incubations: Renewed MoU with AquaInformatics ('22) Reforms in stores	Played a major role in establishing 'SARVAH Incubation Foundation' — a section-8 non-profit company formed for startup and incubation support. SARVAH is an acronym of 'SGGSIET Alumni Run Venture Accelerator Hub' and is inspired from Sanskrit word 'মর্ব:' Incubations: Renewed MoU with SolutionsTouch ('23)

- Awarded "Best Teacher (Urban)" of SRT Marathwada University, Nanded for 2023-24 in recently held function
- Awarded "Chartered Management Institute (CMI) Level 5 Certificate in Management and Leadership" after completing AICTE-UKIERI 'Technical Leadership Development Program' for 2019-20 under mentorship of Dudley College of Technology, England, UK

Research and Development:

- 6 Ph.D. completed, 8 Ph.D. ongoing, 20+ journal publications, 20+ conference publications, 4 book chapters
- Research interests: Biomedical Signal & Image Processing, VLSI design, Enabling Startup Ecosystem, etc.

Research Projects	Development Projects
Completed:	Completed:
■ AICTE RPS as Principal Investigator: Rs.	■ Establishment of 'PG laboratory in VLSI' funded by DST-FIST as Coordinator: Rs. 47.50
6.75 Lakh, Denoising, Compression and	Lakh, 2009-13
Analysis of ECG Signals for Automated	■ Establishment of 'Centre of Excellence in Signal and Image Processing' funded by
Diagnosis, 2012-14	TEQIP-II as Core Member and Procurement Nodal Officer: Rs. 617 Lakh, 2013-21
Ongoing:	■ Establishment of 'Center for VLSI Design and Verification' in collaboration with
■ AICTE RPS as Principal Investigator: Rs.	Mentor Graphics Inc. (Siemens company) as Co-coordinator: Rs. 55 Lakh, 2016-17
15.92 Lakh, Effect of Meditation on	Ongoing:
Physiological Signals, 2020-	■ Establishment of 'AICTE IDEA Laboratory' as Co-coordinator: Rs. 110 Lakh, 2021-

International Visits:

- London, UK, 2017 (Keynote speaker, Session Chair, and presenting papers in International Springer/World Conference on Smart Trends in Systems, Security and Sustainability, London – 'WS4 2017'): February 15-16, 2017
- Glasgow, Scotland, 2017 (Discussions about joint research at University of Glasgow): February 17-18, 2017
- Shanghai, China, 2016 (Session Chair and presenting papers at IEEE/MTS International Conference 'Oceans 2016'): April 10-13, 2016
- Hongkong, China, 2016 (Discussion of research at The State Key Lab of Brain and Cognitive Sciences, Hongkong): April 14-15, 2016
- Kuala Lumpur and Seri Iskandar, Malaysia, 2014 (Attending International Workshop 'WISIBA 2014', and discussions about joint research work at Universiti Teknologi PETRONAS, Malaysia): October 2-5, 2014

Other notable Achievements:

- Team 'NavNirmal'— in which I was mentor of the team won the first prize of Rs. 1 Lakh (shared with another team) in SIH 2022 Hardware edition on the problem given by Govt. of Rajasthan at the finale held at REVA University, Bengaluru
- Worked as editor for proceedings of TEQIP National Conference on "TEQIP-II Best Policies and Practices" organized by SGGSIET,
 Nanded in collaboration with SPFU, Mumbai held during Jan. 10-11, 2014
- Joint research with international collaborators: Akash Gandhmal, Ahmad Fadzil M. Hani, Dileep Kumar, Ruslan Razak, Sanjay Talbar, Suhas Gajre Published 5 papers (4 journal, 1 international conference)

Other Notable Responsibilities:

- Member of MAGIC's Seed Fund Investment Recommendation Committee. This committee has Shri Mukund Bhogale (Bhogale Coating and Paints Pvt. Ltd.) as Chairman, and has other members like Shri Ritesh Mishra, (Mahyco Pvt. Ltd.), 3 Representatives from State Government's Startup Nodal Team, 2 Representatives of a Venture Capital Fund or Angel Network, Domain experts from Industry, and 3 Domain experts from academia. This committee is responsible for distributing seed fund to the startups after carefully going through their needs, current state of health and future plans.
- Working on Board of Research of Swami Ramanand Teerth Marathwada University, Nanded by the nomination from Hon. Governor (and Chancellor), Govt. of Maharashtra since 20-Jul-2023.
- M.Tech. (ES&VLSI) coordinator from 2020-21 to 2022-23.
- President of Institution's Innovation Council (IIC) an initiative of Ministry of Education of SGGSIET Nanded: '18 to '23.
- In-charge of Engineering Exploration Lab and Technology Innovation and Entrepreneurship Center of SGGSIET Nanded: '18 to '23.
- Subject Expert Member, RRC for the BoS in Computer Engineering, University of Mumbai since 31-Aug-2019.
- Expert Member for Electronics and Telecom. Engg. DRC for G. H. Raisoni CEM, Wagholi, Pune since 24-Jan-2020.
- Worked as Member of BoS in SRTMU, Nanded in the board in Electronics Engineering during 30-October-2012 to 13-June-2015.
- Senate member of Institute academic senate/council since 2005.
- Worked as Core Member and Procurement Nodal Officer in 'Centre of Excellence in Signal and Image Processing' under TEQIP-II.
- Worked as Editor of institute annual magazine Drishti in 2016 this magazine won second prize in the university.
- Worked as Faculty Incharge of Drama Club 'Gandharva'.
- Worked as website in-charge of official website of institute during 2007 to 2012 built the first website for institute in HTML.

Industry/Industry Organizations/Startup Ecosystem Connect:

- Intel, Mentor Graphics (now Siemens), Sankalp Semi, AIC-RMP, CMIA-MAGIC, GIZ-MASSIA, NIMA, IGTR, DISQ, Bharat Forge, Endress+Hauser, Expert Gloabal, SAI Tech, Theia, Chipspirit, EvolvingX, Garje Marathi Global (GMG) Innovation, Symbiosis CEI, SIBIC, Yuva IncubatEd (K.I.T.E.S.), BYST, ENSIN Forum, YIN by Sakal Media, Eklaksha, Soctronics, ...
- A few notable activities:
 - Coordinated two-day workshop for faculty members on "Entrepreneurship and Skill Development" jointly organised by SGGSIET Nanded along with IIIT, Pune, and Shiksha Sanskriti Utthan Nyas, New Delhi.
 - Coordinated 2 one-week ATAL-FDPs on 'Enabling Entrepreneurship and Start-up Mindset' in collaboration with MAGIC Aurangabad in July and August, 2021
 - Encouraged and supported 7 students so far (3 working now) for training at DISQ, Nashik a TCS Foundation initiative open social innovation center working for developing social entrepreneurs (using digital technologies to address social challenges)
 - Coordinated 3-days FDP in collaboration with Intel on 'Embedded System Design using Intel SoC FPGAs'
 - Received 22 FPGA kits worth Rs. 1.25 Lakh as a part of Intel FPGA Academic Program
 - Attended Industrial Training on 'Custom VLSI Design Flow on SPICE, Laker ADP and Laker Layout tools' by Sankalp Semiconductors, Ltd., Hubballi during Jun 5-22, 2007
 - Attended 26th International Conference for VLSI Design and 12th International Conference for Embedded Systems organized by VLSI Society of India with Faculty Fellowship Award in 2013 (Also attended in 2018 without fellowship)

Notable workshops/conferences attended/organised:

- Coordinated two-day workshop on 'Entrepreneurship and Skill Development' jointly organised by SGGSIET Nanded along with IIIT, Pune, and Shiksha Sanskriti Utthan Nyas, New Delhi. Other coordinators were: Dr. R.R. Manthalkar, Dr. M.B. Kokare, and Dr. Chandrakant Guled (IIIT Pune)
- Coordinated 6-week Summer Internship in summer of 2018 and 2019 with a theme of 'Ideas to Product Design to Development'
- Coordinated 4 Design Thinking Workshops and 2 Ideation Workshops so far since 2019
- Coordinated 3-days Online Workshop on 'SEQUEL and Other Simulation Tools for Circuits' in collaboration with Department of Electrical Engg., Government Polytechnic, Nanded with Prof. M.B. Patil, IIT Bombay is expert speaker
- Coordinated 2-day Workshop on 'Engineering Projects using Arduino' at GP, Nanded during Dec 19-20, 2022 for final year students of Dept of EE student team gave mentorship to poly students in a true spirit of handholding by guiding their final sem projects
- Worked as Organising Chair for International Conference on Signal & Image Processing (IConSIP) 2016 organized by SGGSIET Nanded in association with IIT Bombay, COE Pune, SVUCOE Tirupati and technically co-sponsored by IEEE Bombay section

Connect with Alumni/Society/Non-profit organisations:

- Worked closely with alumni to establish section-8 company SARVAH which will support incubation and entrepreneurship activities
- Member of MAGIC's Seed Fund Investment Recommendation Committee (Shri Mukund Bhogale as Chairman)
- Mentor at Guruji Education Foundation, a not-for-profit initiative for overall development of students
- Mentor at 'Uth Kalam Utha' an initiative for bottom of pyramid students by Gurudwara Board, Nanded under the mentorship of Dr. P.S. Pasricha, IPS, Former Commissioner of Mumbai Police, and Former Director General of Police of Maharashtra state
- Mentor at GMG Innovation Academy, an initiative of Garje Marathi Global (GMG) since 2021
- Working with Young Inspirators Network (YIN) a Sakal Media Group initiative for overall development of students
- Worked as Secretary of SGGS Engineers' Alumni Association (SEAA) since 1-Jan-2018 to 24-Dec-2019, now Treasurer since Dec. 2022
- Worked in Executive Council of SGGS Engineers' Alumni Association (SEAA) since Jan. 2010 till Dec. 2019 (10 Years)
- Worked as *Alumni Meet coordinator* in 2012